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U.S. Patent Application No.: 09/839,636

Confirmation No.: 2156

Application of: Amin et al.

Art Unit: 2826

Filed: April 20, 2001

Examiner: Scott R. Wilson

For: QUANTUM BIT WITH A MULTI-  
TERMINAL JUNCTION AND  
LOOP WITH A PHASE SHIFTAttorney Docket No.: 706700-999113  
(formerly 11090-035-999)*Message*OFFICIAL FILINGInformation Disclosure Statement

## Enclosures:

1. Information Disclosure Statement, in duplicate - 4 pages;
2. List of References Cited by Applicant - 1 page; and
3. Copies of six (6) cited references - 39 pages.

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Amin et al.	Confirmation No.:	2156
Serial No.:	09/839,636	Art Unit:	2826
Filed:	April 20, 2001	Examiner:	Scott R. Wilson
For:	QUANTUM BIT WITH A MULTI-TERMINAL JUNCTION AND LOOP WITH A PHASE SHIFT	Attorney Docket No.:	706700-999113 (formerly 11090- 035-999)

## INFORMATION DISCLOSURE STATEMENT

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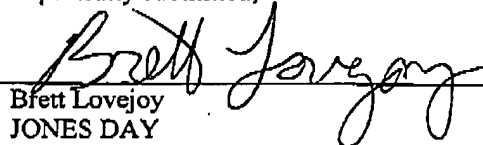
In accordance with the duty of disclosure provisions of 37 C.F.R. §1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the application.

1. Enclosures accompanying this Information Disclosure Statement are:
  - 1a. ☒ A list of all patents, publications, applications, or other information submitted for consideration by the office.
  - 1b. A legible copy of :
    - ☐ Each U.S. patent application publication and U.S. and foreign patent;
    - ☒ Each publication or that portion which caused it to be listed on the PTO-1449;
    - ☐ For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it to be listed on the PTO-1449 including any claims directed to that portion;
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2. ☐ This Information Disclosure Statement is filed under 37 C.F.R. §1.97(b):
  - ☐ Within three months of the filing date of a national application other than a continued prosecution application under §1.53(d);

- ☐ Within three months of the date of entry of the national stage as set forth in §1.491 in an international application;
- ☐ Before the mailing of the first Office action on the merits;
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3. ☐ This Information Disclosure Statement is filed under 37 C.F.R. §1.97(c) after the period specified in 37 C.F.R. §1.97(b), but before the mailing date of any of a final action under 37 C.F.R. §1.113, a notice of allowance under 37 C.F.R. §1.311 or an action that otherwise closes prosecution in the application.
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Respectfully submitted,

Date: July 8, 2004

  
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VIA FACSIMILE TO FAX NO. 571-273-1925

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of:	Amin et al.	Confirmation No.:	2156
Serial No.:	09/839,636	Art Unit:	2826
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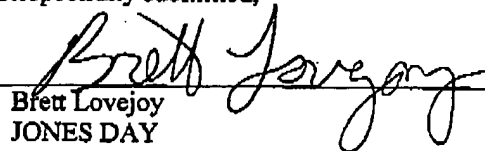
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    - ☐ For each cited pending U.S. application, the application specification including the claims, and any drawing of the application, or portion of the application which caused it to be listed on the PTO-1449 including any claims directed to that portion;
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Respectfully submitted,

Date: July 8, 2004

  
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Sheet 1 of 1

# **LIST OF REFERENCES CITED BY APPLICANT** (Use several sheets if necessary)

ATTY DOCKET NO.

11090-035-999

APPLICATION NO

09/839,636

APPLICANT

Amin et al.

FILING DATE

April 20, 2001

GROUP

2826

## **U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## **FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO

## **OTHER REFERENCES** (Including Author, Title, Date, Pertinent Pages, Etc.)

BR	International Search Report for PCT/CA 02/00532
BS	Blais <i>et al.</i> , "Operation of universal gates in a solid-state quantum computer based on clean Josephson junctions between <i>d</i> -wave superconductors," <i>Physical Review A</i> , April, 2000, Vol. 61, 042308: 1-4
BT	Blatter <i>et al.</i> , "Quantum computing with superconducting phase qubits," <i>Physica C</i> , April 2001, Vol. 352, pp. 105-109
BU	Jonker <i>et al.</i> , "On Quantum & Classical Computing with Arrays of Superconducting Persistent Current Qubits," <i>IEEE, Proceedings of Fifth IEEE International Workshop on Computer Architectures for Machine Perception</i> , September 2000, pp 69-78
BV	Makhlin <i>et al.</i> , "Nano-Electronic Circuits as Quantum Bits," 2000 IEEE International Symposium on Circuits and Systems - Emerging Technologies for the 21 <sup>st</sup> Century, May 2000, pp. 241-244
BW	Vleeming <i>et al.</i> , "Measurements of the flux, embraced by the ring of a four-terminal SQUID, as a function of the external magnetic flux and the applied transport current," <i>Physica B</i> , April 1999, Vol. 262, pp. 296-305

EXAMINER

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